

## GP60001A IST COUPON DESIGN INFORMATION (Back-Plane)

(With capacitance holes)

This package contains a design file for a generic Interconnect Stress Test (IST) Test Coupon containing **Plated Through Hole** Technology, on a 5.0" (126.9 mm) X 0.75" (19 mm) coupon with a total thickness greater than .125" (3.2mm) for Back-Plane style PWB's with a minimum of 10 layers. This coupon is designed with a .060" (1.5mm) grid. The maximum drilled hole for the Power circuit is .030" (.76mm) and pad size is .042" (1.06mm). On the Sense circuit the maximum drilled hole is .016" (.41mm) and pad size is .028" (.71mm). Decreasing these feature sizes will not affect the coupon functionality, but increasing these feature sizes may result in shorts or excessively small clearances. Instructions on how to create a custom IST coupon for a specific product are included.

Note: If this generic design is not suitable to construct a coupon consistent with the actual product, custom coupon design services are available from PWB.

