

## GT40800D IST COUPON INFORMATION (Through Hole)

(With capacitance holes)

This package contains design files for a generic Interconnect Stress Test (IST) Test Coupon containing **Plated Through Hole** Technology on a 5.0" (126.9mm) X 0.7" (17.8mm) coupon with a total thickness up to and not exceeding .125" (3.2mm) and limited to a minimum of six layers. This coupon is designed on two independent grid sizes, .040" (1mm) and .080" (2mm). The maximum hole/pad size for the .040" (1mm) grid is .015" (.38mm) drilled and pad size is .028" (.7mm), on both power and sense circuits. The maximum hole/pad size for the .080" (2mm) grid is .056" (1.42mm) drilled and pad size is .068" (1.73mm), on the power circuit. The geometries for the sense circuit on the .080" (2mm) grid are usually consistent to those used in the .040" (1mm) grid. Decreasing these feature sizes will not affect the coupon functionality but increasing these feature sizes may result in shorts or excessively small clearances. Instructions on how to create a custom IST coupon for a specific product are included.

Note: If this generic design is not suitable to construct a coupon consistent with the actual product, custom coupon design services are available from PWB.

